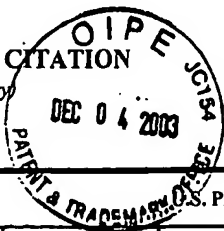


INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

ELPIDA WN-2632

Application Number

10/714,768

Applicant(s)

ISHIKAWA

Filing Date

November 17, 2003

Group Art Unit

2816

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
DZ		6,369,627	4.9.02	TOMITA	327	158	

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
DZ		2001-111394	4.20.01	JAPAN (w/abstract)	H03K	5/135		
DZ		2001-056723	2.27.01	JAPAN (w/abstract)	G06F	1/10		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

DZ		"A 1-Gb/s/pin 512-Mb DDRII SDRAM Using a Digital DLL and a Slew-Rate-Controlled Output Butter" Matano et al., Article, 3 pgs.

EXAMINER

Thalok

DATE CONSIDERED

1/13/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.